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Electrical Engineering Reference Manual is the most comprehensive reference available for the electrical and computer engineering PE exam.

The operational amplifier ("op amp") is the most versatile and widely used type of analog IC, used in audio and voltage amplifiers, signal conditioners, signal converters, oscillators, and analog computing systems. Almost every electronic device uses at least one op amp. This book is Texas Instruments' complete professional-level tutorial and reference to operational amplifier theory and applications. Among the topics covered are basic op amp physics (including reviews of current and voltage division, Thevenin's theorem, and transistor models), idealized op amp operation and configuration, feedback theory and methods, single and dual supply operation, understanding op amp parameters, minimizing noise in op amp circuits, and practical applications such as instrumentation amplifiers, signal conditioning, oscillators, active filters, load and level conversions, and analog computing. There is also extensive coverage of circuit construction techniques, including circuit board design, grounding, input and output isolation, using decoupling capacitors, and frequency characteristics of passive components. The material in this book is applicable to all op amp ICs from all manufacturers, not just TI. Unlike textbook treatments of op amp theory that tend to focus on idealized op amp models and configuration, this title uses idealized models only when necessary to explain op amp theory. The bulk of this book is on real-world op amps and their applications; considerations such as thermal effects, circuit noise, circuit buffering, selection of appropriate op amps for a given application, and unexpected effects in passive components are all discussed in detail. *Published in conjunction with Texas Instruments *A single volume, professional-level guide to op amp theory and applications *Covers circuit board layout techniques for manufacturing op amp circuits.

This book shows that digitally assisted analog to digital converters are not the only way to cope with poor analog performance caused by technology scaling. It describes various analog design techniques that enhance the area and power efficiency without employing any type of digital calibration circuitry. These techniques consist of self-biasing for PVT enhancement, inverter-based design for improved speed/power ratio, gain-of-two obtained by voltage sum instead of charge redistribution, and current-mode reference shifting instead of voltage reference shifting. Together, these techniques allow enhancing the area and power efficiency of the main building blocks of a multiplying digital-to-analog converter (MDAC) based stage, namely, the flash quantizer, the amplifier, and the switched capacitor network of the MDAC. Complementing the theoretical analyses of the various techniques, a power efficient operational transconductance amplifier is implemented and experimentally characterized. Furthermore, a medium-low resolution reference-free high-speed time-interleaved pipeline ADC employing all mentioned design techniques and circuits is presented, implemented and experimentally characterized. This ADC is said to be reference-free because it precludes any reference voltage, therefore saving power and area, as reference circuits are not necessary. Experimental results demonstrate the potential of the techniques which enabled the implementation of area and power efficient circuits.

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